

## ABSTRACT OF THE DISCLOSURE

There is provided an image display device operating in response to the input of digital picture signals, in which the occupied area of a signal line driver circuit thereof is reduced, and the parasitic capacitance and resistance of input transmission lines of the digital picture signals are reduced. The device includes both a unit for directly inputting the digital picture signals to shift registers and for performing series parallel conversion, and a unit for causing  $n$  ( $n$  is a natural number not less than 2) signal lines to jointly own storage circuits and D/A converter circuits in the signal line driver circuit. One horizontal scan period is divided into  $n$  periods, and the storage circuits and the D/A converter circuits perform a processing to signal lines different in each of the divided periods.